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semiconductor and (nanoscale same roughness)	0

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DATE: Wednesday, February 16, 2005 [Printable Copy](#) [Create Case](#)

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DB=EPAB; PLUR=YES; OP=ADJ

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DB=USPT; PLUR=YES; OP=ADJ

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DB=PGPB; PLUR=YES; OP=ADJ

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DB=TDBD; PLUR=YES; OP=ADJ

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L1 6417060.pn.

1 L1

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